

APPARATUS OF REPAIRING MEMORY CELL AND METHOD THEREFOR

ABSTRACT

The present invention provides a semiconductor memory cell repairing apparatus and
5 method that can effectively repair memory cells although a various type of failures in
memory cells are generated when a rule in the failure is detected.

There is provided a memory cell repairing apparatus includes state fixing parts
operable to fix a specific external input address signal out of external input addresses as a
constant state, address input receivers operable to receive output signals of the state selecting
10 part and external input signals, address code selecting part operable to receive output address
signals of the address input receiver and to convert output address signal path through a
control signal which selects address code change to output a changed address signal, and
address input buffers operable to receive the changed address signal outputted from the
address code selecting part to output a new internal address.